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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/044,155	01/11/2002	Coralyn S. Gauvin	01-600	1096	
75	90 01/10/2005		EXAM	EXAMINER	
LSI Logic Corporation			THOMPSON, ANNETTE M		
Intellectual Property Law Department M/S D-106			ART UNIT	PAPER NUMBER	
1551 McCarthy Boulevard Milpitas, CA 95035			2825		
			DATE MAILED: 01/10/2005		

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No.	Applicant(s)			
Office Action Summary		10/044,155	GAUVIN, CORALYN S.			
		Examiner	Art Unit			
		A. M. Thompson	2825			
Period f	The MAILING DATE of this communication app or Reply	<u> </u>	correspondence address			
A SH THE - Exto afte - If th - If N - Fail Any	HORTENED STATUTORY PERIOD FOR REPL' MAILING DATE OF THIS COMMUNICATION. ensions of time may be available under the provisions of 37 CFR 1.1. or SIX (6) MONTHS from the mailing date of this communication. he period for reply specified above is less than thirty (30) days, a reply operiod for reply is specified above, the maximum statutory period of ure to reply within the set or extended period for reply will, by statute or reply received by the Office later than three months after the mailing ned patent term adjustment. See 37 CFR 1.704(b).	36(a). In no event, however, may a reply be ti y within the statutory minimum of thirty (30) da vill apply and will expire SIX (6) MONTHS fron , cause the application to become ABANDONE	mely filed ys will be considered timely. In the mailing date of this communication. ED (35 U.S.C. § 133).			
Status						
1)⊠	Responsive to communication(s) filed on 18 O	<u>ctober 2004</u> .				
-		action is non-final.				
3)□	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.					
Disposit	tion of Claims					
5)□ 6)⊠ 7)□	Claim(s) 1-14 is/are pending in the application. 4a) Of the above claim(s) is/are withdray. Claim(s) is/are allowed. Claim(s) 1-14 is/are rejected. Claim(s) is/are objected to. Claim(s) are subject to restriction and/o	wn from consideration.				
Applicat	tion Papers					
9)⊠	The specification is objected to by the Examine	r.				
·-	10) The drawing(s) filed on is/are: a) accepted or b) objected to by the Examiner.					
	Applicant may not request that any objection to the	drawing(s) be held in abeyance. Se	e 37 CFR 1.85(a).			
11)	Replacement drawing sheet(s) including the correct The oath or declaration is objected to by the Ex	• • • • • • • • • • • • • • • • • • • •	•			
Priority	under 35 U.S.C. § 119					
a)	Acknowledgment is made of a claim for foreign All b) Some * c) None of: 1. Certified copies of the priority documents 2. Certified copies of the priority documents 3. Copies of the certified copies of the priority application from the International Bureau See the attached detailed Office action for a list	s have been received. s have been received in Applicat rity documents have been receiv a (PCT Rule 17.2(a)).	ion No ed in this National Stage			
Attachmer	nt(s)					
	ce of References Cited (PTO-892)	4) Interview Summary				
3) 🔲 Infor	ce of Draftsperson's Patent Drawing Review (PTO-948) rmation Disclosure Statement(s) (PTO-1449 or PTO/SB/08) er No(s)/Mail Date	Paper No(s)/Mail D 5) Notice of Informal F 6) Other:	ate Patent Application (PTO-152)			

DETAILED ACTION

Applicant's <u>Amendment</u> to 10/044,155 has been examined. Claims 7-14 are added. Claims 3-6 are amended. Claims 1-14 are pending.

Specification

1. The specification is objected to as failing to provide proper antecedent basis for the claimed subject matter. See 37 CFR 1.75(d)(1) and MPEP § 608.01(o). Correction of the following is required: Antecedent basis is lacking for the claim terminology of "live signals" in claims 8, 10, 12, and 14.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

Rejection of claims 1-14

- 3. Claims 1-14 are rejected under 35 U.S.C. 102(e) as being anticipated by Dervisoglu et al. (Dervisoglu), U.S. Publication 2003/0131327.
- 4. Pursuant to claim 1-3, Dervisoglu discloses designating a plurality of test points in the IC from which the test signals are generated (¶¶ 12-14); designating a hierarchy of a plurality of regional levels and sub-levels within the IC(¶19); distributing a hierarchy of a plurality of multiplexers across the IC (Fig. 2), each multiplexer being local to one of

the regional levels and sublevels (¶ 23); connecting a lowest level portion of the multiplexers to the test points to receive the test signals (¶23); connecting a mid-level portion of the multiplexers to other multiplexers to receive selected portions of test signals therefrom (claims 1 and 18); and connecting a highest level one of the multiplexers to the output locations to supply a final one of the selected portion of the test signals (claims 1 and 18).

- 5. Pursuant to claims 4 and 5, Dervisoglu discloses the integrated circuit product on which the method of claims 1-3 operates (Figs. 2, 8B, and 10).
- 6. Pursuant to claim 6 wherein each selecting means includes a multiplexer (¶ 16, ¶98).
- 7. Pursuant to claims 7, 9, 11 and 13, wherein the test points are internal to the circuitry of the IC ($\P\P$ 4, 7, 52-54).
- 8. Pursuant to claims 8, 10, 12, 14, interpreting these claims as "wherein the test signals are signals from the internal test points. . ." (¶¶ 54, 55).

Remarks

9. Dervisoglu reads on Applicant's claimed limitations. First of all, Dervisoglu discloses the existence of *internal* test features. The external I/O pins connect to the internal core on an IC (see at least ¶ 44). Second, Applicant's specification provides no antecedent basis for "live signals" and it is unclear what Applicant's intend to claim. If "live" means *active*, then certainly Dervisoglu discloses or at least suggests active (as well as inactive) signals. Finally, Applicant asserts that the claims call for outputting in parallel. However, Examiner does not find where Applicant's claims recites any

limitation regarding outputting test signals in parallel. But even if that were the case, Dervisoglu would still be applicable here, where at least at ¶ 45 of Dervisoglu recites applying and capturing test stimuli in parallel.

Conclusion

10. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

11. Any inquiry concerning this communication or earlier communications should be directed to Examiner A.M. Thompson whose telephone number is (571) 272-1909. The Examiner can usually be reached Monday thru Friday from 8:00 a.m. to 4:30 p.m..

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should

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you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

12. Responses to this action should be mailed to the appropriate mail stop:

Mail Stop _____

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

or faxed to:

(703) 872-9306, (for all **OFFICIAL** communications intended for entry)

A. M. NHOMPSON
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